VISVESVARAYA COLLEGE OF ENGINEERING AND TECHNOLOGY

M.P. PATELGUDA (V), IBRAHIMPATNAM (M), RANGAREDDY DIST-501510 AN UGC-AUTONOMOUS INSTITUTION

EXAMINATION BRANCH

II B.TECH I SEMESTER (VR24) REGULAR EXAMINATIONS, DEC-2025

TIMETABLE

TIME: FN: 10.00 AM TO 01.00 PM

BRANCH	DATE AND DAY						
	20-12-202 FN SATURDAY	22-12-2025 FN MONDAY	24-12-2025 FN WEDNESDAY	29-12-2025 FN MONDAY	31-12-2025 FN WEDNESDAY		
CIVIL ENGINEERING (01-CE)	Probability and Statistics	Building Materials, Construction and Planning	Engineering Geology	Strength of Materials – I	Fluid Mechanics		
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	Numerical Methods and Complex variables	Electrical Machines-I	Power System-I	Electro Magnetic Fields	Analog Electronic Circuits		
MECHANICAL ENGINEERING (03-ME)	Probability, Statistics & Complex Variables	Mechanics of Solids	Production Technology	Thermodynamics	Metallurgy & Material Science		
ELECTRONICS COMMUNICATI ONS & ENGINEERING (04-ECE)	Numerical Methods and Complex Variables	Analog Circuits	Network analysis and Synthesis	Signals and Systems	Digital Logic Design		

DATE: 12-12-2025

VISVESVARAYA COLLEGE OF ENGINEERING AND TECHNOLOGY

M.P. PATELGUDA (V), IBRAHIMPATNAM (M), RANGAREDDY DIST-501510 AN UGC-AUTONOMOUS INSTITUTION

EXAMINATION BRANCH

II B.TECH I SEMESTER (VR24) REGULAR EXAMINATIONS, DEC-2025

TIMETABLE

TIME: FN: 10.00 AM TO 01.00 PM

		DATE AND DAY				
BRANCH	20-12-202 FN	22-12-2025 FN	24-12-2025 FN	29-12-2025 FN	31-12-2025 FN	
COMPUTER SCIENCE & ENGINEERING (05-CSE)	Computer Oriented Statistical Methods.	MONDAY Digital Electronics	Object Oriented Programming through Java	MONDAY Data Structures	Computer Organization and Architecture	
COMPUTER SCIENCE AND ENGINEERING (ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING) (66-CSE(AI&ML)	Mathematical and Statistical Foundations	Software Engineering	Operating Systems	Data Structures	Computer Organization and Architecture	
COMPUTER SCIENCE AND ENGINEERING (DATASCIENCE) (67-CSE(DS)	Computer Oriented Statistical Methods	Digital Electronics	Object Oriented Programming through Java	Data Structures	Computer Organization and Architecture	



CONTROLLER OF EXAMINATIONS

Principal & Chief Superintendent
Visvesvart Active of Engg & Tech.
M.P. Patelguda (V), 10 rahimpatnam (M;